



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)	
)	
ITO)	
)	Art Unit 2825
Application Number: 10/770,479)	
)	Examiner
Filed: February 4, 2004)	Thompson, Annette M.
)	
For: INTEGRATED CIRCUIT, INTEGRATED)	
CIRCUIT DESIGN METHOD AND)	
HARDWARE DESCRIPTION GENERATION)	
METHOD TO GENERATE HARDWARE)	
BEHAVIOR DESCRIPTION OF INTEGRATED)	
CIRCUIT)	
)	
Attorney Docket No. NITT.0189)	
)	
Honorable Assistant Commissioner		
for Patents		
Washington, D.C. 20231		

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the office action dated May 18, 2005 in the above identified application, the period for response to which is set to expire on September 18, 2005, with a one-month extension fee filed herewith. Please amend the above identified application as follows: